

A

B

C

D

E

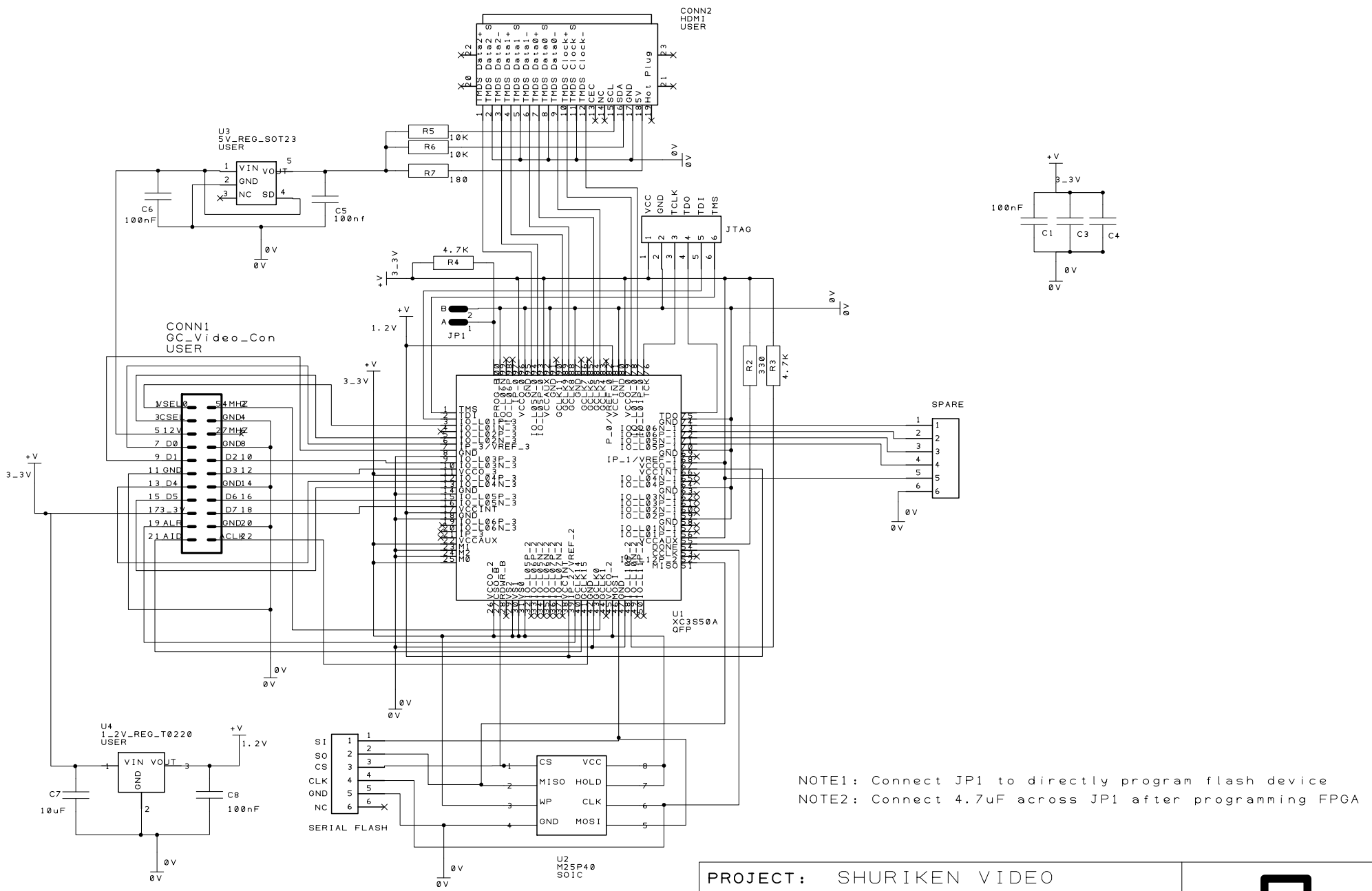
F

1

2

3

4



NOTE1: Connect JP1 to directly program flash device
 NOTE2: Connect 4.7uF across JP1 after programming FPGA

PROJECT: SHURIKEN VIDEO
 DRAWING TITLE:
 DRAWN BY: TEVEN TAFFS REVISION: 3
 SHEET: 1 STATUS: PROTO
 DATE: 3/1/2016 DESIGNSPARK PCB available for FREE at [www.](http://www.designspark.com)

